

## CLAIMS

What is claimed is:

1. A computer core comprising at least one interface signal that is configurable such that the interface signal can be selectively present.
2. The computer core as set forth in claim 1, wherein at least one interface signal is further configured to support different levels of functionality.
3. The computer core as set forth in claim 2, wherein a signal width of at least one interface signal is configurable to support different signal widths.
4. A computer core comprising at least one interface signal that is configurable such that the interface signal can be configured to support different levels of functionality.
5. The computer core as set forth in claim 4, wherein a signal width of at least one interface signal is configurable to support different signal widths.
6. A computer core comprising at least one interface signal, a signal width of the at least one interface signal being configurable to support different signal widths.
7. A method for generating a computer core interface comprising:  
providing configurable source code representative of the computer core and identifying parameters of the computer interface;  
defining configuration parameters of the computer core interface; and  
generating a computer core from the configurable source code comprising an interface comprising the identified parameters configurable in accordance with the defined configuration parameters.
8. The method as set forth in claim 7, wherein at least one of the configuration parameters are selected from the group comprising parameters that define whether the signal is present in the interface, define different levels of

functionality of the interface, and define a signal width of at lease one interface signal.

9. A method for generating a core with a configured interface comprising:

[illegible]